REMARKS

Claims 1-11 remain pending. Claims 1 and 6 are independent claims.

Reconsideration of the above-identified application, as amended and in view of the following remarks, is respectfully requested.

The specification stands objected to for reciting "that" instead of "there".

Applicants wish to thank examiner to noting this informalities. In response, the specification has been amended to recite "there". Accordingly, withdraw of objection is respectfully requested.

Claims 1-11 stand rejected under 35 U.S.C. 102(b) as anticipated by U.S. Patent No. 4,661,946 to Takahashi et al. ("Takahashi"). Applicant respectfully traverses this rejection.

As indicated in the background, the prior art converter <u>indiscriminately</u> converts every channel signal in the multi-channel signals during a conversion of two different modulated signals. As such, the present invention has been provided to convert a desired channel signal(page 3, lines 13-21). This problem is addressed according to the teachings provided in claim 1, which, in part, recites:

a channel selector for generating a <u>channel select signal for identifying</u>

said at least one channel of the multiple channels in said system, and . . .

a plurality of mixers for selectively releasing and transmitting said input

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digital signal modulated by said one of said two different modulation standards and said converted input digital signal to the other one of said two different modulation standards by said codec means in response to said channel select signal."

Takahashi <u>fails to teach or disclose</u> the need to convert <u>a specific channel</u> of the multi channel signals, as in the present invention. In fact, Takahashi also converts <u>every channel signal indiscriminately</u>. Specifically, Takahashi teaches a digital switch module having an encoding law conversion memory and a control memory, in which a selector is provided to execute transmission of control signals in a time division slot manner so that the control memory can designate which of the regions in the encoding law conversion memory is to be used during each time slot.

Accordingly, Takahashi fails to show how "mixer" performs "selectively releasing and transmitting" of "said input digital signal modulated by said one of said two different modulation standards and said converted input digital signal to the other one of said two different modulation standards by said codec means" occurs "in response to said channel select signal." Thus, the mixer is used in conjunction with the codec 210 to selectively convert the specified channels.

In contrast, the memory 24 of Takahashi performs the actual encoding law conversion between the A-law and the u-law trunks, so that voice signal and non-voice signals can be converted selectively.

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Accordingly, it is respectfully submitted that claim 1 is not anticipated by Takahashi.

In order for a reference to anticipate a claim, the reference must disclose all of the elements recited by the rejected claims.

The Court of Appeals held in *Verdegaal Bros. v. Union Oil Co. of California*, 814 F.2d 628,631, 2 USPQ2d 1051, 1053 (Fed. Cir. 1987) that:

A claim is anticipated only if each and every element as set forth in the claim is found, either expressly or inherently described, in a single prior art reference.

Accordingly, reconsideration and withdrawal of this ground of rejection are respectfully requested.

Claim 6 recites the same above-quoted limitations, and is likewise deemed to be patentable over Takahashi. As to the other claims, each depends from a base claim and is deemed to be patentable over the cited references at least due to its dependency, although each warrants further consideration based on its additional, individual merits.

In view of the foregoing amendments and remarks, it is believed that this application is now in condition for allowance. The Examiner is invited to contact the undersigned in the event of any perceived outstanding issues so that passage of the case to issue can be effected without the need for a further Office Action.

In the event that any additional fee is required to continue the prosecution of this Application as requested, please charge such fee to Deposit Account No. 502-470.

Respectfully submitted,

CHA & REITER

By: Steve S. Cha
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ignature and Date)

Date: September 1, 2005

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to P.O. BOX AF, COMMISSIONER FOR PATENTS, ALEXANDRIA, VA 22313 on September 1, 2005.

Steve Cha, Reg. No. 44,069 (Name of Registered Rep.)